AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1-14 (Canceled)

15. (Currently amended): A computer readable storage medium, having thereon a computer program to modify a layout pattern of an embedded array in a semiconductor integrated circuit~~, in which~~ with basic cells ~~are~~ arranged in a matrix, said computer program comprising an operational step of modifying layout pattern data of said embedded array by detecting and removing a portion of an impurity region in a basic cell based on layout data of contact holes, the basic cell constituting a circuit ~~by detecting and removing a non-use area of a basic cell based on layout data of contact holes~~.

16. (Original): A storage medium according to claim 15, wherein said contact holes are for connection between an impurity region of transistors and a line thereover, said program comprises the step of:

segmenting a pattern of said impurity region into individual segmentation patterns each as a removal unit, each segmentation pattern corresponding with one or more of all individual contact hole patterns that can be arranged; and

judging one of said removal units as said non-use area if an individual contact hole pattern corresponding with said one of said removal unit is not existent.

17. (New): A computer readable storage medium comprising a computer program thereon, said computer program including operational steps of:

providing a normal layout pattern of an embedded array in a semiconductor integrated circuit, said normal layout pattern comprising basic cells arranged in a matrix,

receiving data of a non-use area of a basic cell; and

producing a modified layout pattern by removing said non-use area of said basic cell from said normal layout pattern.

18. (New): A computer readable storage medium, having thereon a computer program to modify a layout pattern of an embedded array in a semiconductor intergraded circuit with basic cells arranged in a matrix, wherein said computer program comprising an operational step of modifying layout pattern data of said embedded array by detecting and removing a non-use area in a basic cell based on layout data which represents that a contact hole is not existent, the basic cell constituting a circuit.